Chapter 4

The Von Neumann architecture uses a single processor which follows a linear sequences of fetch-decode-execute. This means that is follows a line by line sequence when executing the code.

Though this has a problem when large amounts of instructions are to be processed but it has to run through line by line before it reaches the end, this is called the “Von Neumann bottleneck”.

Question: The Neumann architecture is a system which includes a RAM component in between of the CPU and the hard disk of the computer which serves as a temporary memory storage. It is focused on using a single processor and follows a linear sequence for the fetch-decode-execute cycle.

The Registers and the Fetch decode execute cycle